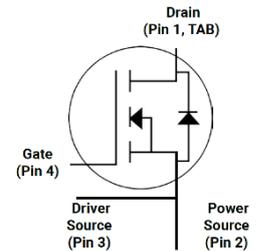
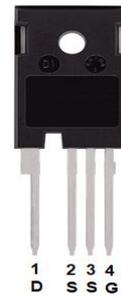


Product Summary

$V_{DS} = 1200\text{ V}$
 $I_D@25^\circ\text{C} = 64\text{ A}$
 $R_{DS(ON)} = 36\text{ m}\Omega$



TO-247-4

Features

- High Blocking Voltage
- High Frequency Operation
- Low on-resistance
- Fast intrinsic diode with low reverse recovery

Applications

- Motor Drives
- Solar / Wind Inverters
- EV Charging Station

Benefits

- Higher System Efficiency
- Parallel Device Convenience without thermal runaway
- High Temperature Application
- Hard Switching & Higher Reliability
- Easy to drive
- AC/DC converters
- DC/DC converters
- Uninterruptable power supplies

Maximum Ratings ($T_C=25^\circ\text{C}$ unless otherwise specified)

| Parameter | Symbol | Test conditions | Value | Unit |
|---------------------------------|----------------|---|------------|------------------|
| Drain - Source Voltage | V_{DSmax} | $V_{GS}=0\text{V}, I_D=100\mu\text{A}$ | 1200 | V |
| Gate - Source Voltage (dynamic) | V_{GSmax} | AC ($f>1\text{ Hz}$) | -10 / +25 | V |
| Gate - Source Voltage (static) | V_{GSop} | static | -5 / +20 | V |
| Continuous Drain Current | I_D | $V_{GS} = 20\text{V}, T_C=25^\circ\text{C}$ $V_{GS} = 20\text{V}, T_C=100^\circ\text{C}$ | 64 45 | A |
| Pulsed Drain Current | $I_{D(pulse)}$ | $T_C=25^\circ\text{C}$ | 112 | A |
| Short Circuit Capability | t_{SC} | $V_{DD}=800\text{V}, V_{GS}=20\text{V}$ | 3 | μS |
| Short Circuit Capability | I_{DS} | $V_{DD}=800\text{V}, V_{GS}=20\text{V}$ | 600 | A |
| Total power dissipation | P_D | $T_C=25^\circ\text{C}$ | 300 | W |
| Operating Junction Temperature | T_J | | -55 to 175 | $^\circ\text{C}$ |
| Storage Temperature | T_{STG} | | -55 to 175 | $^\circ\text{C}$ |

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

Electrical Characteristics (T_C=25°C unless otherwise specified)

| Parameter | Symbol | Test conditions | Min | Typ | Max | Unit |
|----------------------------------|----------------------|---|---|------|-----|------|
| Drain-Source Breakdown Voltage | V _{(BR)DSS} | V _{GS} = 0V, I _D = 100μA | 1200 | | | V |
| Gate Threshold Voltage | V _{GS(th)} | V _{DS} = V _{GS} , I _D = 10mA | 1.8 | 2.8 | 3.9 | V |
| | | V _{DS} = V _{GS} , I _D = 10mA, T _J = 150°C | | 1.9 | | |
| | | V _{DS} = V _{GS} , I _D = 10mA, T _J = 175°C | | 1.8 | | |
| Zero Gate Voltage Drain Current | I _{DSS} | V _{DS} = 1200V, V _{GS} = 0V | 0 | 5 | 100 | μA |
| Gate-Source Leakage Current | I _{GSS} | V _{GS} = 20V, V _{DS} = 0V | 0 | 10 | 200 | nA |
| Gate-Source Leakage Current | I _{GSS} | V _{GS} = -5V, V _{DS} = 0V | -200 | -10 | 0 | nA |
| Drain-Source On-State Resistance | R _{DS(on)} | V _{GS} = 18V, I _D = 40 A | | 41 | | mΩ |
| | | V _{GS} = 18V, I _D = 40 A, T _J = 150°C | | 61 | | |
| | | V _{GS} = 18V, I _D = 40 A, T _J = 175°C | | 68 | | |
| | | V _{GS} = 20V, I _D = 40 A | | 36 | 50 | |
| | | V _{GS} = 20V, I _D = 40 A, T _J = 150°C | | 58 | | |
| | | V _{GS} = 20V, I _D = 40 A, T _J = 175°C | | 65 | | |
| Transconductance | g _{fs} | V _{DS} = 20V, I _D = 40 A, | | 23 | | S |
| | | V _{DS} = 20V, I _D = 40 A, T _J = 150°C | | 20 | | |
| | | V _{DS} = 20V, I _D = 40 A, T _J = 175°C | | 20 | | |
| Input capacitance | C _{iss} | V _{DS} = 1000V, V _{GS} = 0V f = 1MHz | | 2980 | | pF |
| Output capacitance | C _{oss} | | | 143 | | |
| Reverse transfer capacitance | C _{rss} | | | 15 | | |
| C _{oss} Stored Energy | E _{oss} | | | 92 | | |
| Total gate charge | Q _g | V _{DS} = 800V, V _{GS} = -5V / 20V I _D = 40 A, | | 148 | | nC |
| Gate-source charge | Q _{gs} | | | 40 | | |
| Gate-drain charge | Q _{gd} | | | 70 | | |
| Internal gate input resistance | R _{g(int)} | f = 1MHz, I _D = 0A | | 2 | | Ω |
| Turn-On Switching Energy | E _{ON} | V _{DS} = 800 V, V _{GS} = -5V/20V, I _D = 40A, R _{G(ext)} = 2Ω, L = 200μH | | 240 | | μJ |
| Turn-Off Switching Energy | E _{OFF} | | | 80 | | |
| Turn-On Delay Time | t _{d(on)} | | | 12 | | ns |
| Rise Time | t _r | | | 16 | | |
| Turn-Off Delay Time | t _{d(off)} | | | 27 | | |
| Fall Time | t _f | | | 7 | | |
| Avalanche Capability | E _{AS} | | V _{DD} = 100V, V _{GS} =20V, L=2mH | 400 | | |
| Avalanche Capability | I _{AV} | V _{DD} = 100V, V _{GS} =20V, L=2mH | 20 | | | A |

Reverse Diode Characteristics (T_C=25°C unless otherwise specified)

| Parameter | Symbol | Condition | Min | Typ | Max | Unit |
|----------------------------------|------------------|--|-----|-----|-----|------|
| Diode Forward Voltage | V _{SD} | V _{GS} = -5V, I _{SD} = 20A, | | 3.9 | | V |
| | | V _{GS} = -5V, I _{SD} = 20A, T _J = 150°C | | 3.7 | | |
| | | V _{GS} = -5V, I _{SD} = 20A, T _J = 175°C | | 3.6 | | |
| Continuous Diode Forward Current | I _S | V _{GS} = -5V | | | 60 | A |
| Reverse Recovery time | t _{rr} | V _{GS} = -5V, I _{SD} = 40A, V _R = 800V, dif/dt = 4200 A/μs | | 17 | | ns |
| Reverse Recovery Charge | Q _{rr} | | | 310 | | nC |
| Peak Reverse Recovery Current | I _{rrm} | | | 30 | | A |

Thermal Characteristics

| Parameter | Symbol | Condition | Min | Typ | Max | Unit |
|---------------------------------|----------------------|---------------|-----|-----|-----|------|
| Thermal Resistance (per device) | R _{th(j-c)} | junction-case | | 0.4 | 0.5 | °C/W |

Typical Performance

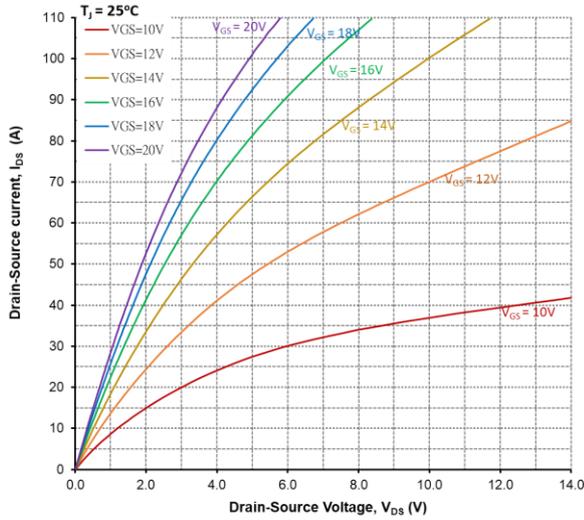


Figure 1. Output Characteristics, $T_J = 25^\circ\text{C}$

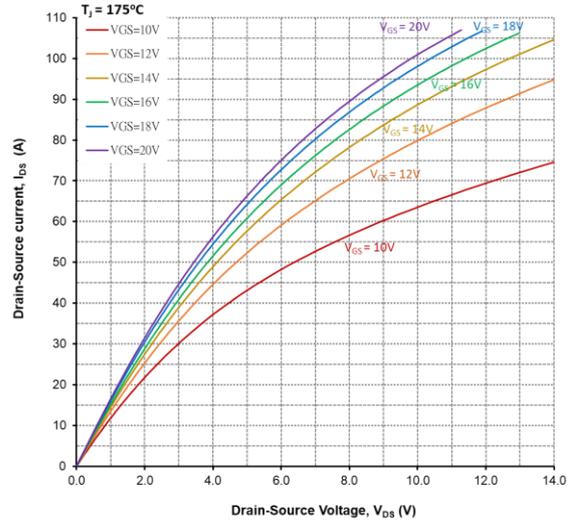


Figure 2. Output Characteristics, $T_J = 175^\circ\text{C}$

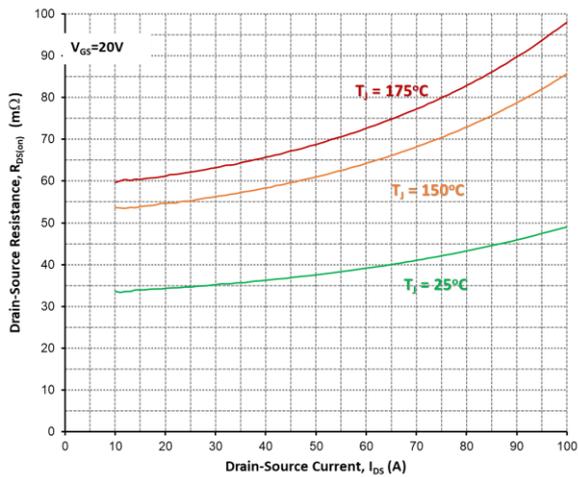


Figure 3. On-Resistance vs. Drain Current For Various Temperatures

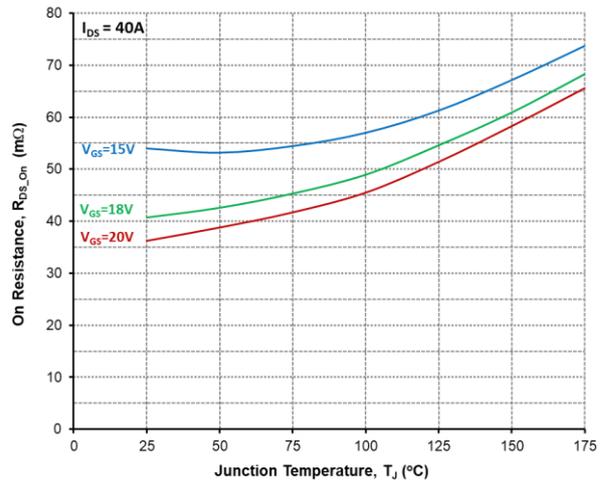


Figure 4. On-Resistance vs. Temperature

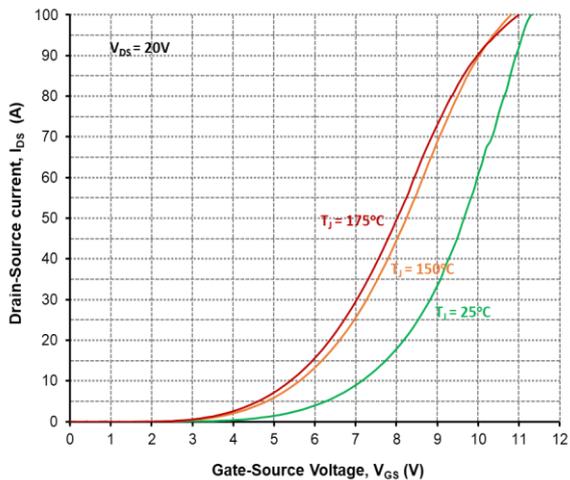


Figure 5. Transfer Characteristic For Various Junction Temperatures

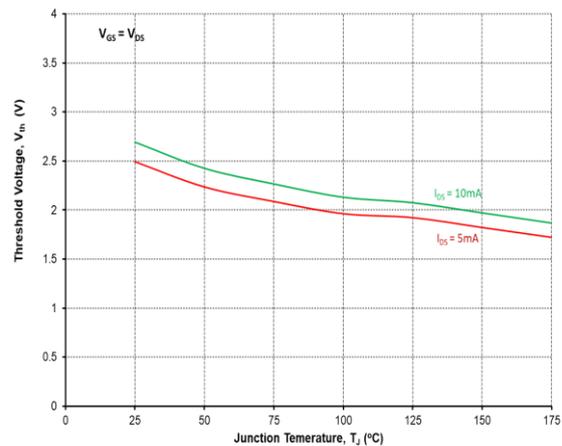


Figure 6. Threshold Voltage vs. Temperature

Typical Performance

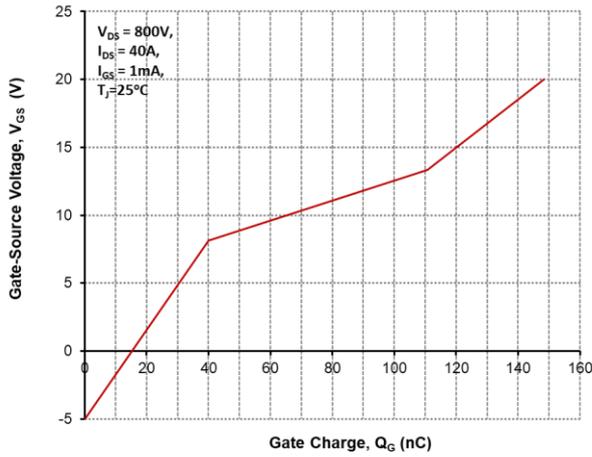


Figure 7. Gate Charge Characteristics

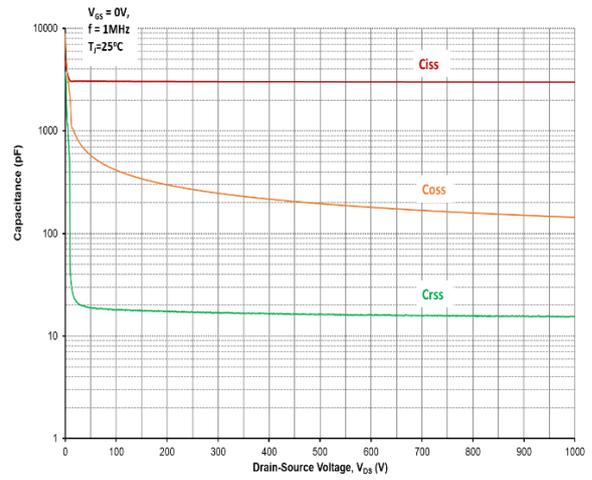


Figure 8. Capacitances vs. Drain-Source Voltage (0-1000V)

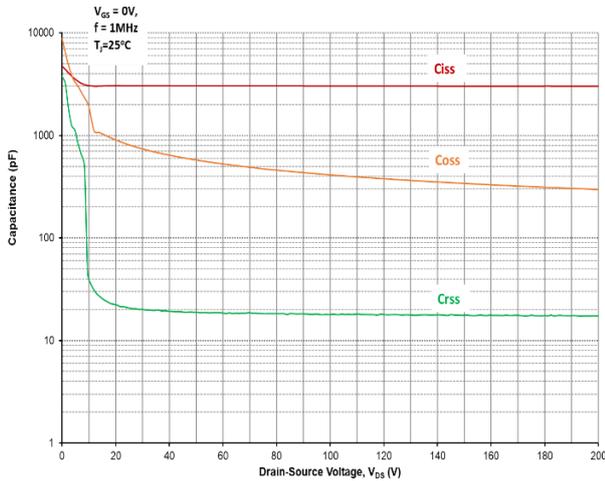


Figure 9. Capacitances vs. Drain-Source Voltage (0-200V)

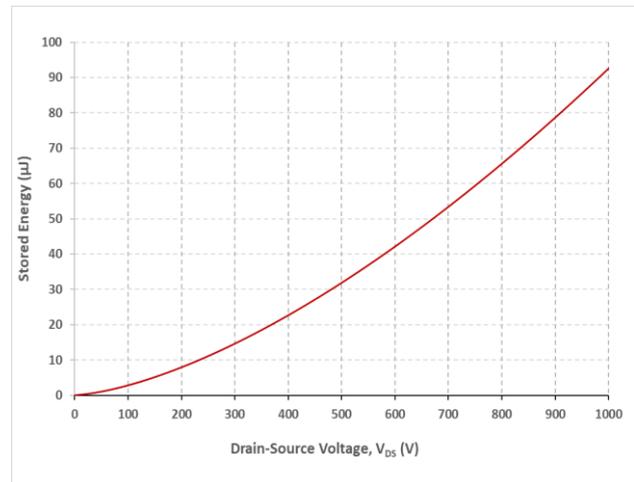


Figure 10. Output Capacitor Stored Energy

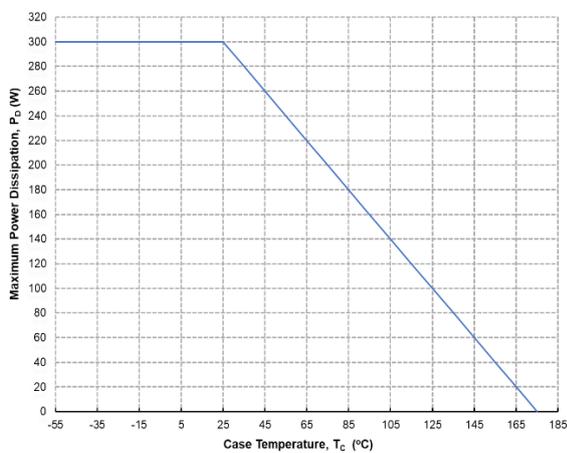


Figure 11. Maximum Power Dissipation Derating vs. Case Temperature

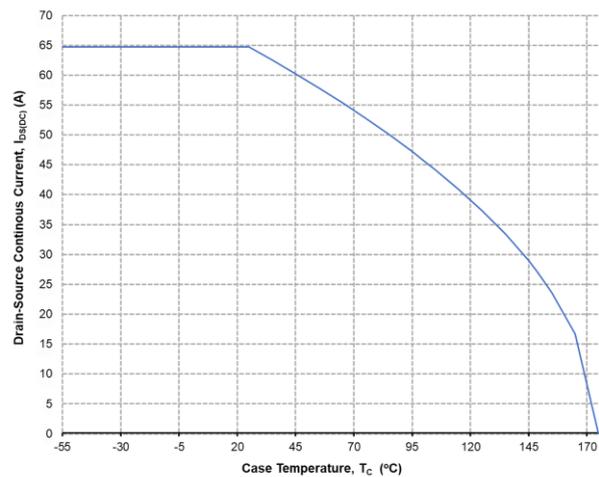


Figure 12. Continuous Drain Current Derating vs. Case Temperature

Typical Performance

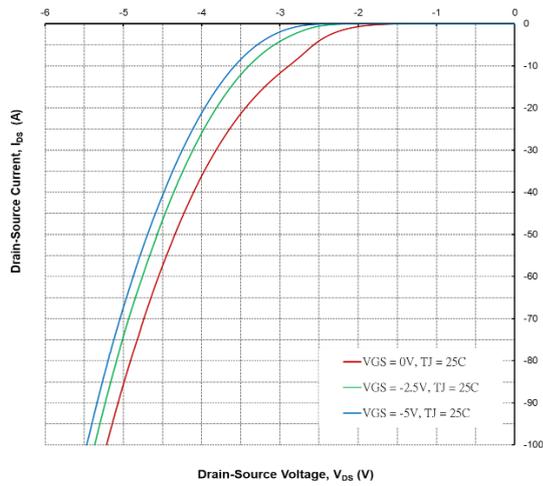


Figure 13. Body Diode Characteristics @ 25°C

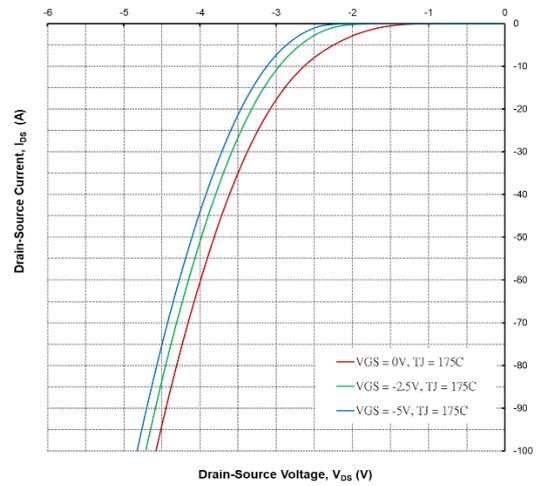


Figure 14. Body Diode Characteristics @ 175°C

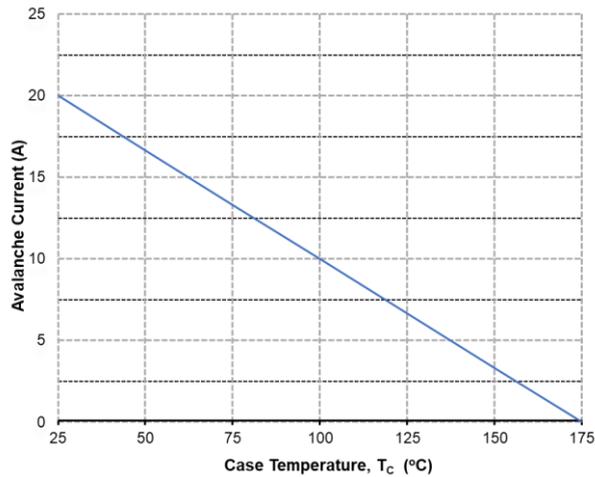


Figure 15. Single Avalanche vs. Temperature

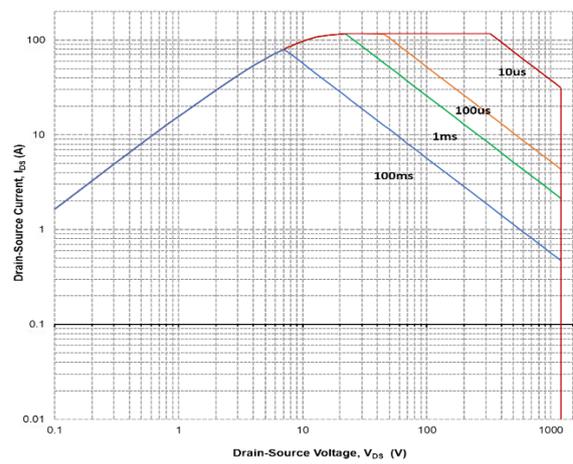
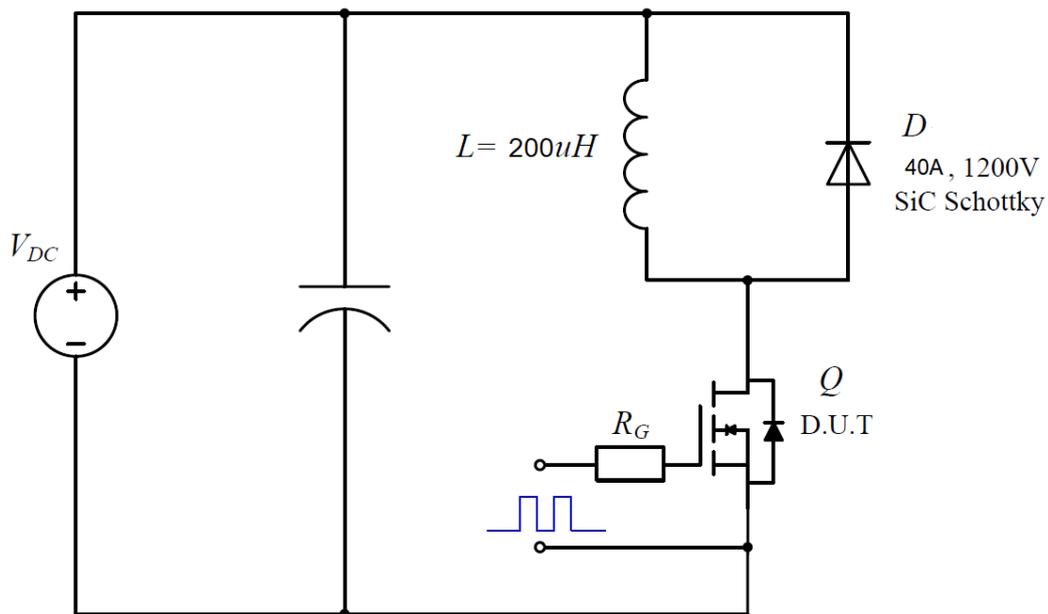
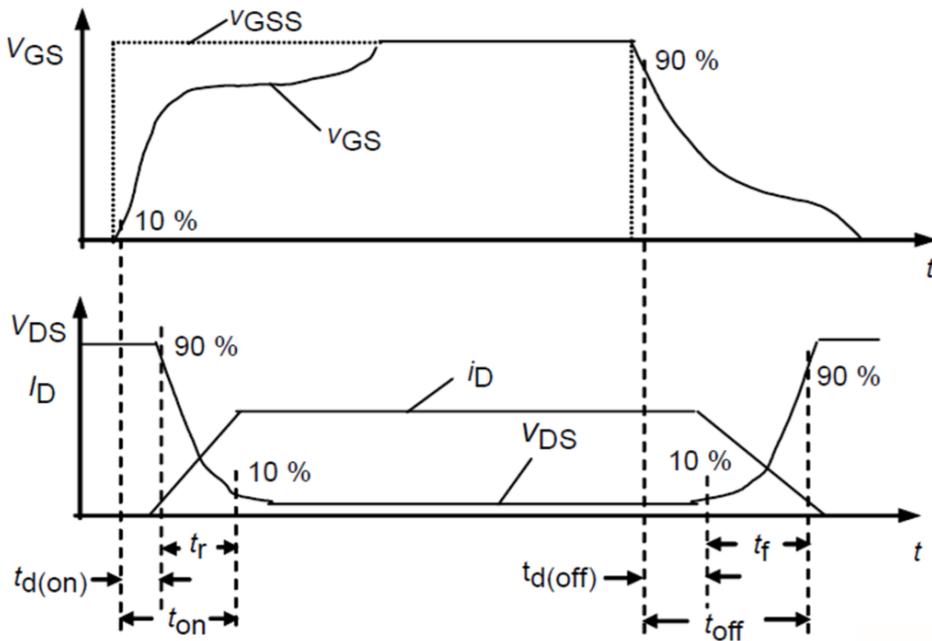


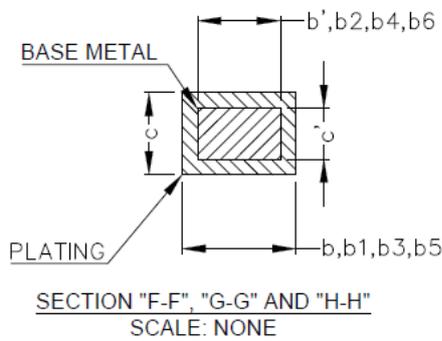
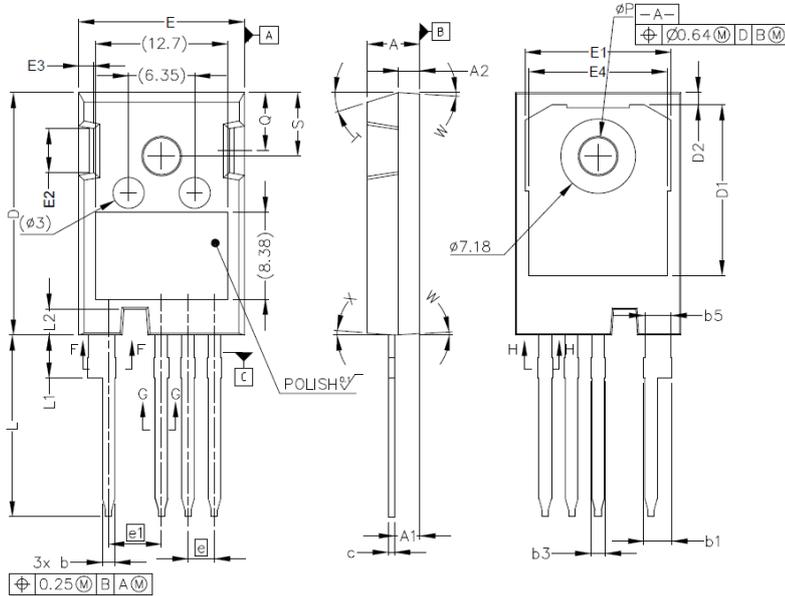
Figure 16. Safe Operating Area

Switching Times Definition and Test Circuit



Package Dimensions

(TO-247-4 Package)



| SYMBOL | MILLIMETERS | |
|--------|-------------|-------|
| | MIN | MAX |
| A | 4.83 | 5.21 |
| A1 | 2.29 | 2.54 |
| A2 | 1.91 | 2.16 |
| b' | 1.07 | 1.28 |
| b | 1.07 | 1.33 |
| b1 | 2.39 | 2.94 |
| b2 | 2.39 | 2.84 |
| b3 | 1.07 | 1.60 |
| b4 | 1.07 | 1.50 |
| b5 | 2.39 | 2.69 |
| b6 | 2.39 | 2.64 |
| c' | 0.55 | 0.65 |
| c | 0.55 | 0.68 |
| D | 23.30 | 23.60 |
| D1 | 16.25 | 17.65 |
| D2 | 0.95 | 1.25 |
| E | 15.75 | 16.13 |
| E1 | 13.10 | 14.15 |
| E2 | 3.68 | 5.10 |
| E3 | 1.00 | 1.90 |
| E4 | 12.38 | 13.43 |
| e | 2.54 BSC | |
| e1 | 5.08 BSC | |
| N | 4 | |
| L | 17.31 | 17.82 |
| L1 | 3.97 | 4.37 |
| L2 | 2.35 | 2.65 |
| øP | 3.51 | 3.65 |
| Q | 5.49 | 6.00 |
| S | 6.04 | 6.30 |
| T | 17.5° REF. | |
| W | 3.5° REF. | |
| X | 4° REF. | |

NOTE :
 1. ALL METAL SURFACES: TIN PLATED, EXCEPT AREA OF CUT
 2. DIMENSIONING & TOLERANCING CONFIRM TO ASME Y14.5M-1994.
 3. ALL DIMENSIONS ARE IN MILLIMETERS. ANGLES ARE IN DEGREES.