

Product Summary

V_R = 1200 V I_F = 20A (T_C=150°C) Qc = 93nC (V_R=800V)



Features

- Zero Forward/Reverse Recovery Current
- High Blocking Voltage
- High Frequency Operation
- Positive Temperature Coefficient on V_F
- Temperature Independent Switching Behavior
- High surge current capability

- ApplicationsMotor Drives
- Solar / Wind Inverters

Benefits

- Higher System Efficiency
- Parallel Device Convenience without thermal runaway
- Higher Temperature Application
- No Switching loss
- Hard Switching & Higher Reliability
- Environmental Protection
- AC/DC converters
- DC/DC converters
- Uninterruptable power supplies

Maximum Ratings (Tc=25°C unless otherwise specified)

Parameter	Symbol	Test conditions	Value	Unit
Peak Repetitive Reverse Voltage	V _{RRM}		1200	V
Continuous Forward Current	IF	Tc=25°C	58	Α
		T _c =135°C	26	
		T _C =150°C	20	
Non repetitive Forward Surge Current	I _{FSM}	$T_{\rm C}$ = 25°C, $t_{\rm p}$ =10 ms,	140	Α
		Half Sine Pulse		
		$T_{c} = 110^{\circ}C$, $t_{p}=10$ ms,	130	
Densetities as als Fernand Orman Ormant		Half Sine Pulse	110	•
Repetitive peak Forward Surge Current	I _{FRM}	$T_c = 25^{\circ}C$, $t_p=10$ ms, Freq = 0.1Hz, 100 cycles,	110	A
		Half Sine Pulse		
		$T_{\rm C} = 110^{\circ}$ C, t _p =10 ms,	100	
		Freq = 0.1 Hz, 100 cycles,	100	
		Half Sine Pulse		
Total power dissipation	PD	Tc=25°C	250	W
		T _C =110°C	108	
Single Pulse Avalanche Energy	E _{AS}	L=2mH, I _{AS} =10A	100	mJ
Diode dv/dt ruggedness	dv/dt	V _R = 0-1200V	80	V/ns
Operating Junction Temperature	ΤJ		-55 to 175	°C
Storage Temperature	T _{STG}		-55 to 175	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

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Electrical Characteristics

Parameter	Symbol	Test conditions	Min	Тур	Max	Unit
DC Blocking Voltage	V _{DC}	T _J = 25°C	1200			V
Forward Voltage	V _F	I _F = 20A, T _J = 25°C		1.45	1.8	V
		I _F = 20A, T _J = 125°C		1.8		V
		I _F = 20A, T _J = 175°C		2.0		V
Reverse Current	I _R	V _R = 1200V, T _J = 25°C		10	200	uA
		V _R = 1200V, T _J = 125°C		20	250	uA
		V _R = 1200V, T _J = 175°C		50	300	uA
Total Capacitive Charge	Qc	V _R = 800V, T _J = 25°C		93		nC
Total Capacitance	С	V _R = 1V, T _J = 25°C, Freq = 1MHz		1120		
		V _R = 400V, T _J = 25°C, Freq = 1MHz		92		pF
		V _R = 800V, T _J = 25°C, Freq = 1MHz		62		

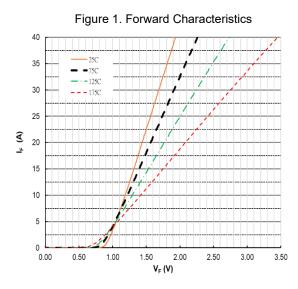
Note: This is a majority carrier diode, so there is no reverse recovery charge

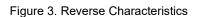
Thermal Characteristics

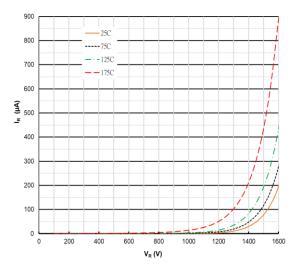
Parameter	Symbol	Condition	Min	Тур	Max	Unit
Thermal Resistance	R _{th(j-c)}	R _{th(j-c)} junction-case		0.6	0.75	⁰ C/W



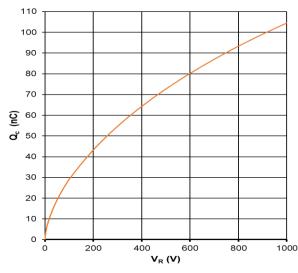
Typical Electrical Curves

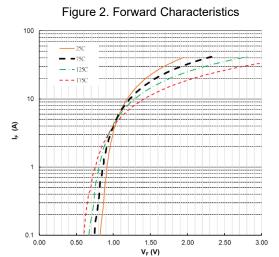




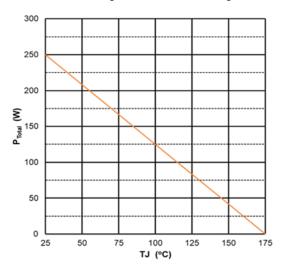


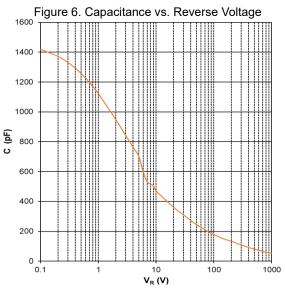












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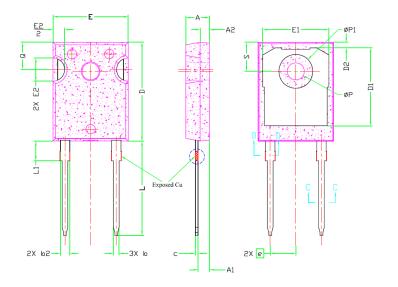
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ACD20PS120A Silicon Carbide Schottky Diode

Package Dimensions

(TO-247-2 Package)



	0			
SYMBOL	MIN.	NOM.	MAX.	NOTES
A	4.83	5.02	5.21	
A1	2.29	2.41	2.55	
A2	1.50	2.00	2.49	
b	1,12	1,20	1,33	
b1	1.12	1.20	1.28	
b2	1.91	2.00	2.39	6
b3	1,91	2,00	2,34	
с	0.55	0.60	0.69	6
c1	0.55	0.60	0.65	
D	20,80	20,95	21,10	4
D1	16.25	16.55	17.65	5
D2	0.51	1.19	1.35	
E	15,75	15,94	16,13	4
E1	13.46	14.02	14.16	5
E2	4.32	4.91	5.49	3
е				
L	19.81	20.07	20.32	
L1	4.10	4.19	4.40	6
ØP	3.56	3,61	3,65	7
ØP1	7.19REF.			
Q	5,39	5,79	6,20	
S	6.04	6.17	6.30	



Note:

- Note:
 1. Package Reference: JEDEC TO247, Variation AD.
 2. All Dimensions Are In mm.
 3. Slot Required, Notch May Be Rounded
 4. Dimension D & E Do Not Include Mold Flash. Mold Flash Shall Not Exceed 0.127mm Pre Side. These Dimensions Are Measured At The Outermost Extreme Of The Plastic Body.
 5. Thermal Pad Contrue Optional Within Dimension D1 & E1

- At The Outermost Extreme Of The Plastic Body.
 5. Thermal Pad Contour Optional Within Dimension D1 & E1.
 6. Lead Finish Uncontrolled In L1.
 7. ØP To Have A Maximum Draft Angle Of 1.5° To The Top Of The Part With A Maximum Hole Diameter Of 3.91 mm.
 8. Dimension "b2" And "b4" Does Not Include Dambar Protrusion. Allowable Dambar Protrusion Shall Be 0.10mm Total In Excess Of "b2" And "b4" Dimension At Maximum Material Condition.

